

(C) Starting on a separate page, a marked-up version entitled: "Version with markings to show changes made."

Amendments

In the Specification:

Please substitute the following paragraphs/sections for the pending paragraphs/sections.

- Substitute pending paragraph 4, with the following paragraph 4:

91
The charge pump 110 outputs a signal "SCP" (charge pump signal) on the basis of the pulse signals ϕR , ϕP output from the phase comparator 106, to a low-pass filter (hereinafter referred to as "LPF") 112. This output signal SCP contains a pulse component in its D.C. component. The D.C. component rises and falls with the frequency changes of the pulse signals ϕR , ϕP , while the pulse component changes on the basis of the phase difference of the pulse signals ϕR , ϕP .

- Substitute pending paragraph 31, with the following paragraph 31:

92
A thick oxide 508 is formed on the region/substrate 504. The thick oxide 508 can comprise silicon dioxide, silicon nitride, or the like, as would be apparent to a person skilled in the semiconductor art. The thick oxide can be formed to a thickness of between about 20 and 100Å (Angstroms). The thick oxide material and specific thickness are application (e.g., depending on the applied voltage) and/or process (e.g., 0.13µm-technology) specific, as would become apparent to a person skilled in the relevant art.

- Substitute pending paragraph 35, with the following paragraph 35:

Q3
The low pass filter 112 of FIG. 1 is used to filter out high frequency components of the SCP signal. According to an embodiment of the present invention, filter 112 can comprise a P-gate NMOS semiconductor device 500, as described above in connection with FIG. 5. The charge pump 110 preferably accumulates electrical charge based on the difference signals and provides a voltage control signal with sufficient current to the VCO 114 to adjust phase and/or frequency of the VCO output.

- Substitute pending paragraph 36, with the following paragraph 36:

Q4
The PLL shown in FIG. 1 is for illustrative purposes only. The present invention can be applied to a PLL circuit, as well as any other circuit including a LPF.

In the Drawings:

Please delete Figure 7 from the application.